

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC
First Named Inventor/Applicant Name:	Hiroshi Hashimoto
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (5B/08)	020244_IDS_Form_SB08_due_02_20_09.pdf	764430 86d53523d4d52508b248bc12d8e3d75d037952	no	4

Warnings:

Information:

2	NPL Documents	020244_European_OA.pdf	272338 <small>/e79dc92014d8eccc8f6c154c24fbc906/11fbb3</small>	no	3
Warnings:					
Information:					
Total Files Size (in bytes):			1036768		
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